P. 1

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Comments:

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U.S. Patent Application Serial No. 10/802,549

Filed: March 16, 2004

For: ADJUSTING DIE PLACEMENT ON A SEMICONDUCTOR

WAFER TO INCREASE YIELD

By: Eitan CADOURI Examiner: P. Dang Group Art Unit: 2818

Our Reference: 52432-20006.00

Attached is the following:

1. Transmittal (1 page)

2. Amendment Under 37 C.F.R. 1.111 (4 pages)

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Application blumber. Application Number 10/802,549 **TRANSMITTAL** Filing Date March 16, 2004 First Named Inventor **FORM** Eitan CADOURI Art Unit 2818 (to be used for all correspondence after Initial filing) Examiner Name P. Dang Attorney Docket Number Total Number of Pages in This Submission Б 524322000600 ENCLOSURES (Check all that apply) After Allowance Communication Fee Transmittal Form Drawing(s) Appeal Communication to Board of Fee Attached Licensing-related Papers Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief) X Amendment/Reply.-4 pgs. Petition Petition to Convert to a After Final Proprietary Information Provisional Application ower of Attorney, Revocation Affidavits/declaration(s) Status Letter Change of Correspondence Address Other. Enclosure(s) (please Extension of Time Request Terminal Disclaimer Identify below): **Express Abandonment Request** Request for Refund 1. Fax Cover Sheet Information Disclosure Statement -CD, Number of CD(s) Certified Copy of Priority Landscape Table on CD Document(s) Reply to Missing Parts/ Remarks Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name MORRISON & FOERSTER LLP (Customer No. 20872) Signature Printed name Peter J. Yim Reg. No. April 7, 2008 44,417

I hereby certify that this cor 8300, on the date shown be Dated: April 7, 2008	respondence is being facsing f	to bettimens of the	>	t and Tradems (Caura Teang)	1k Office, to fax no. 571-273-
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Docket No.: 524322000600

(PATENT)

(Jeallia Isani)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Eitan CADOURI

Dated: April 7, 2008

Application No.: 10/802,549

Confirmation No.: 3719

Filed: March 16, 2004

Art Unit: 2818

For: ADJUSTING DIE PLACEMENT ON A

SEMICONDUCTOR WAFER TO INCREASE

YIELD

Examiner: P. Dang

AMENDMENT UNDER 37 C.F.R. 1.111

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is in response to the non-final Office Action dated January 31, 2006 (Paper No. Part of Paper No./Mail Date 012506), for which a response is due on April 30, 2006. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.